



Layer-1 Conformance Test

HFC-S mini

ISDN S/T Controller

Tektronix K1403 Measurement Protocol

Name of company:	Cologne Chip AG
Test sequence name:	
Name of operator:	K.Jauernik
Organisation unit:	TÜV Rheinland, Product Safety
Device version number:	Tektronix,7KK1403,10201113202011201 0.30412533**2525**4141
Test object name:	HFC-S mini ISDN Sample Board
Test object description:	Tektronix,7KK1403,10201113202011201 0.30412533**2525**4141

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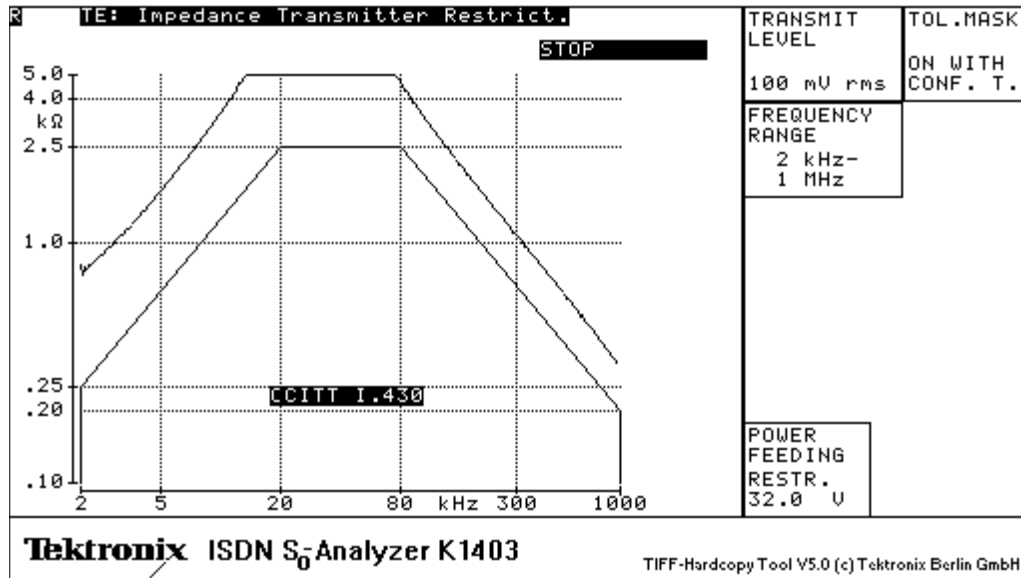
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Impedance Transmitter

1. V30-12.4

Test A: output impedance when transmitting a binary one in state F3, restricted power at 32V

Conformance PASSED



Output Impedance

2. V30-12.8

Test B: output impedance when transmitting a binary zero, positive pulses into a 50R load, restricted power at 32V

double pulses into 50R (R+,R-,R+-)
isolated pulses (R+) w. loop

Conformance PASSED

R(+/-) = 0.000000 OHM	R(+) = 0.000000 OHM	R(-) = 29.998213 OHM
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3. V30-12.12

Test B: output impedance when transmitting a binary zero, negative pulses into a 50R load, restricted power at 32V

double pulses into 50R (R+,R-,R+-)
isolated pulses (R-) w. loop

Conformance PASSED

R(+/-) = 0.000000 OHM	R(+) = 30.509411 OHM	R(-) = 0.000000 OHM
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4. V30-12.16

Test B: output impedance when transmitting a binary zero, positive pulses into a 400 OHM load, restricted power at 32V

double pulses into 400 OHM (R+,R-,R+-)
isolated pulses (R+) w. loop

Conformance PASSED

R(+/-) = 0.000000 OHM	R(+) = 0.000000 OHM	R(-) = 32.090565 OHM
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5. V30-12.20

Test B: output impedance when transmitting a binary zero, negative pulses into a 400R load, restricted power at 32V

double pulses into 400R (R+,R-,R+-)
isolated pulses (R-) w. loop

Conformance PASSED

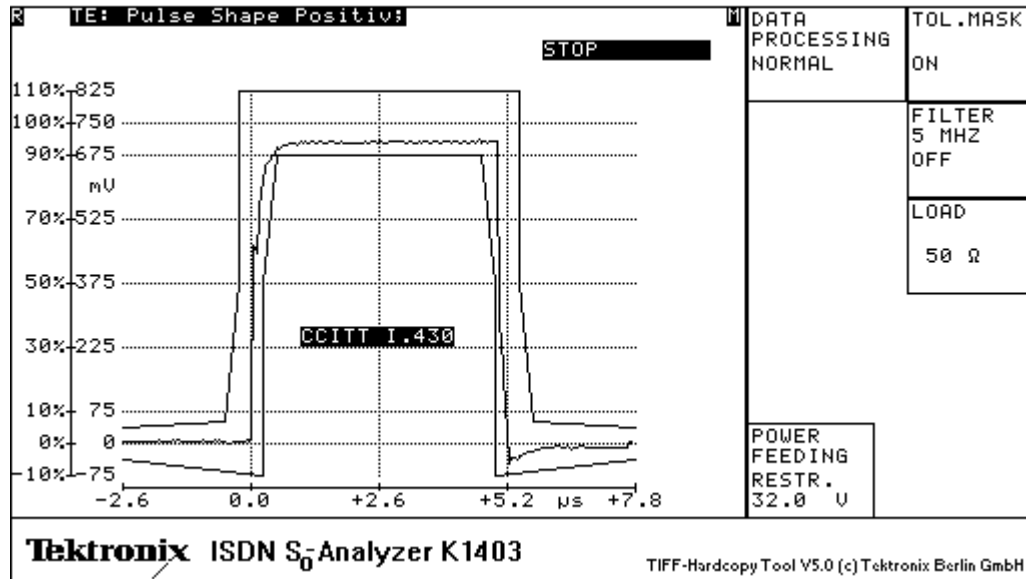
R(+/-) = 0.000000 OHM	R(+) = 31.180626 OHM	R(-) = 0.000000 OHM
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Pulse Shape

6. V30-13.4

Pulse shape and amplitude for positive pulses, restricted power at 32V

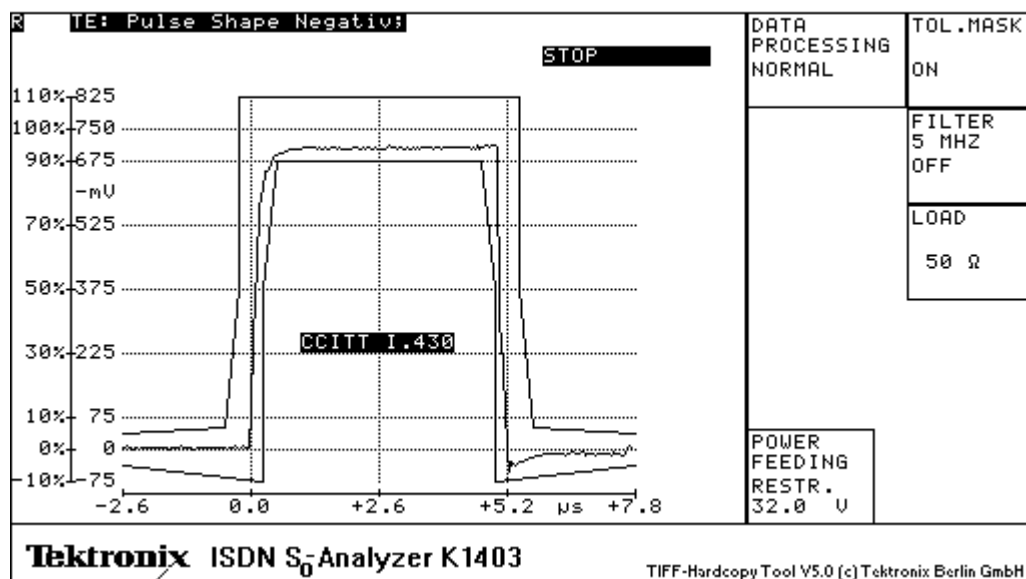
Conformance PASSED



7. V30-13.8

Pulse shape and amplitude for negative pulses, restricted power at 32V

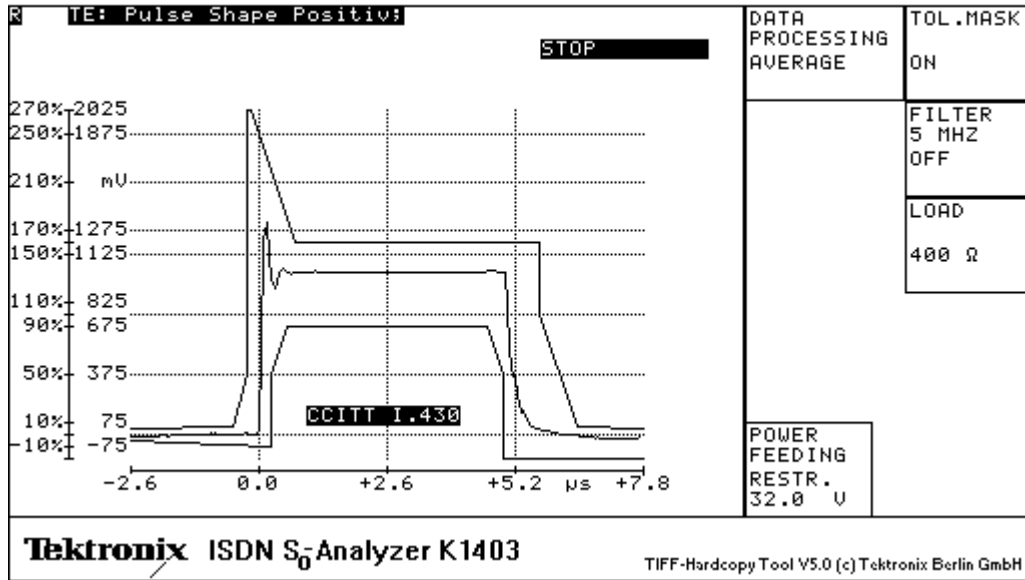
Conformance PASSED



8. V30-13.12

Test A: Voltage on a 400R load (pulse shape) for positive pulses, restricted power at 32V

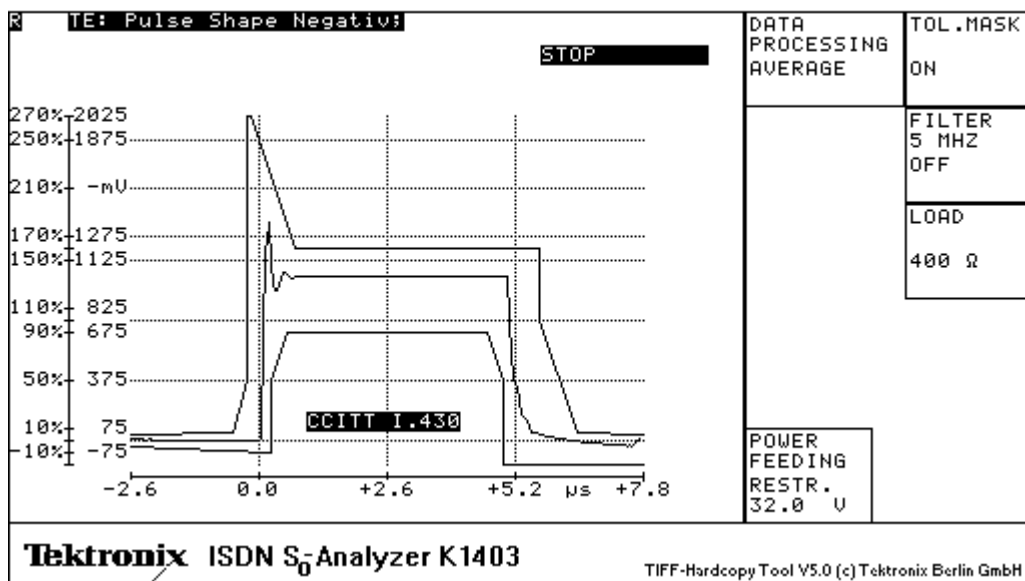
Conformance PASSED



9. V30-13.16

Test A: Voltage on a 400R load (pulse shape) for negative pulses, restricted power at 32V

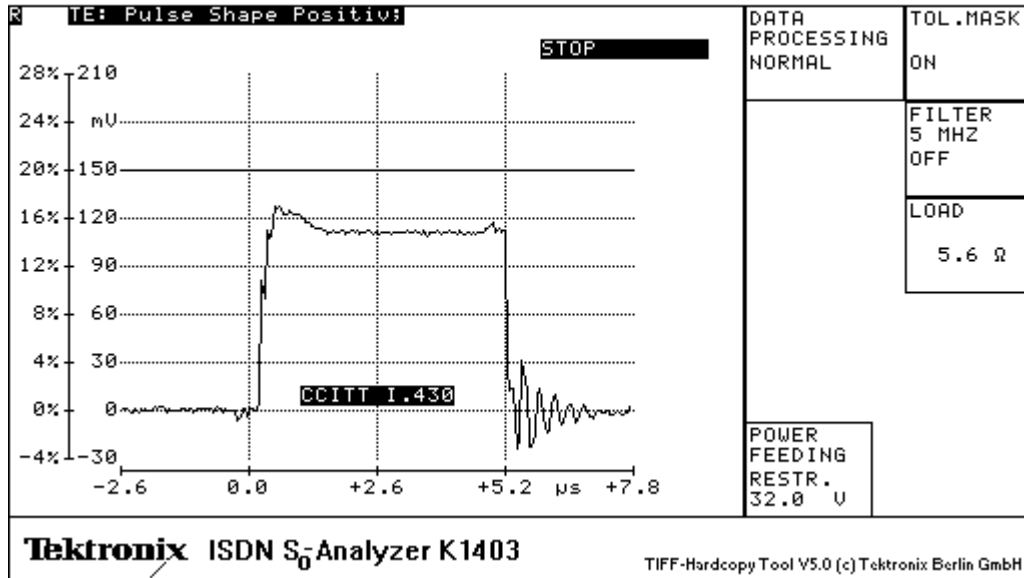
Conformance PASSED



10. V30-13.20

Test B: Voltage on a 5.6R load (pulse shape) for positive pulses, restricted power at 32V

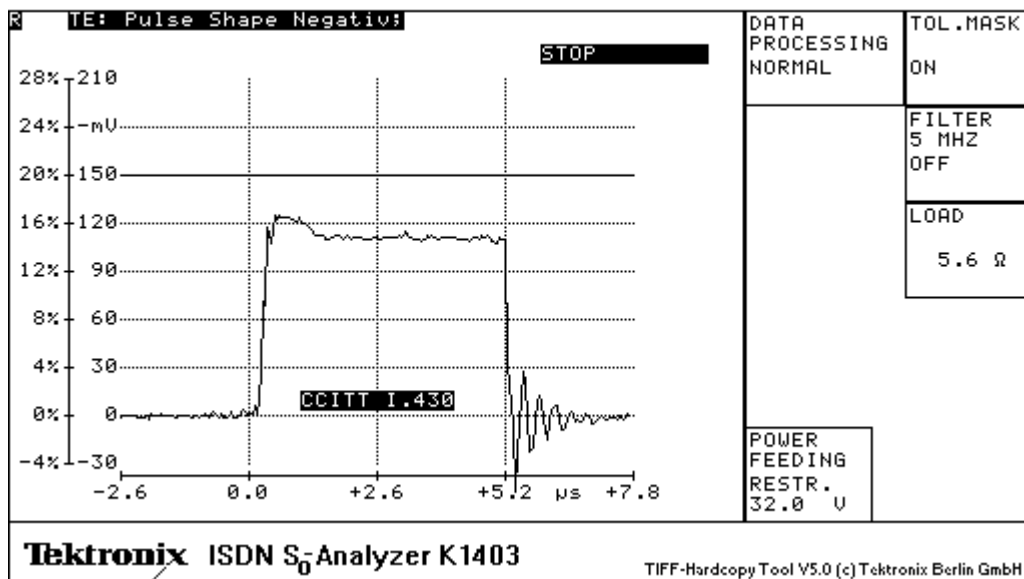
Conformance PASSED



11. V30-13.24

Test B: Voltage on a 5.6W load (pulse shape) for negative pulses, restricted power at 32V

Conformance PASSED



Pulse Amplitude

12. V30-14.1

Pulse amplitude, normal power at 42V

Conformance PASSED

dU+/Unom = -5.926061 %	dU-/Unom = -6.661009 %		
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13. V30-14.2

Pulse amplitude, normal power at 24V

Conformance PASSED

dU+/Unom = -5.920927 %	dU-/Unom = -6.666135 %		
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14. V30-14.4

Pulse amplitude, restricted power at 32V

Conformance PASSED

dU+/Unom = -5.919862 %	dU-/Unom = -6.667201 %		
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Pulse Unbalance of an Isolated Couple of Pulses

15. V30-14.5

Pulse unbalance of an isolated couple of pulses, normal power at 42V

Conformance PASSED

$$df/F_{nom} = 0.838255 \%$$

16. V30-14.6

Pulse unbalance of an isolated couple of pulses, normal power at 24V

Conformance PASSED

$$df/F_{nom} = 0.165803 \%$$

17. V30-14.8

Pulse unbalance of an isolated couple of pulses, restricted power at 32V

Conformance PASSED

$$df/F_{nom} = 0.894959 \%$$

Transmitter Output Longitudinal Conversion Loss

18. V30-15.1

Transmitter output longitudinal conversion loss in state F3, normal power at 42V

Conformance PASSED

19. V30-15.2

Transmitter output longitudinal conversion loss in state F3, normal power at 24V

Conformance PASSED

20. V30-15.3

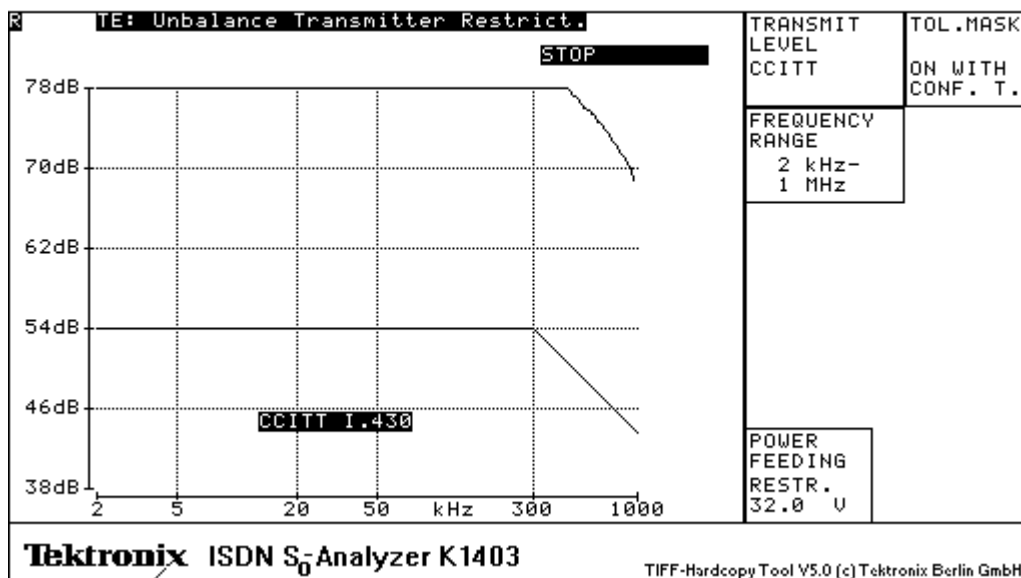
Transmitter output longitudinal conversion loss in state F3, restricted power at 42V

Conformance PASSED

21. V30-15.4

Transmitter output longitudinal conversion loss in state F3, restricted power at 32V

Conformance PASSED

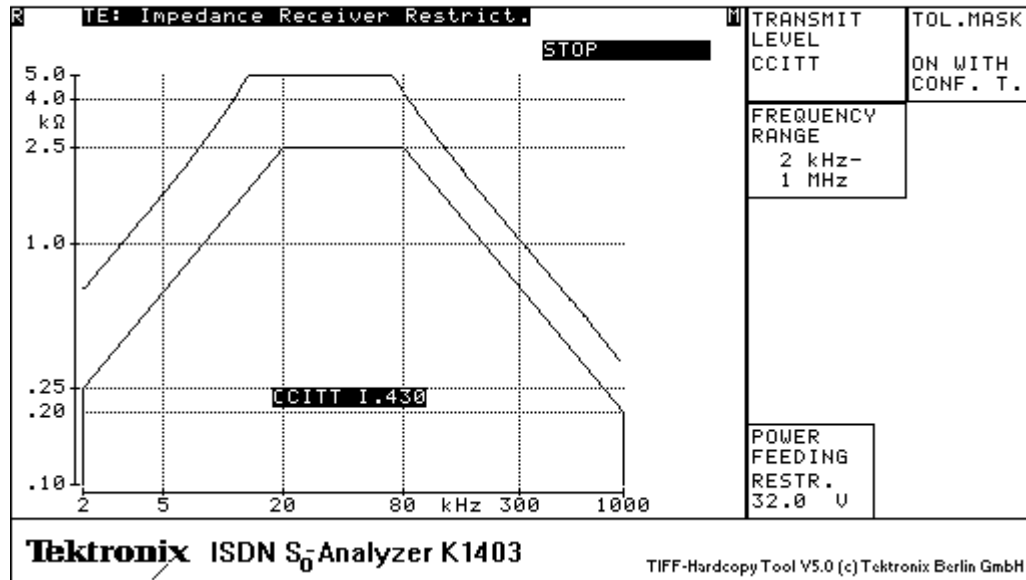


Receiver Input Impedance

22. V30-16.4

Test A: Receiver input impedance in state F3, restricted power at 32V

Conformance PASSED



Receiver Longitudinal Conversion Loss

23. V30-18.1U

Receiver unbalance about earth (longitudinal conversion loss) in state F3, normal power at 42V

Conformance PASSED

24. V30-18.2U

Receiver unbalance about earth (longitudinal conversion loss) in state F3, normal power at 24V

Conformance PASSED

25. V30-18.3U

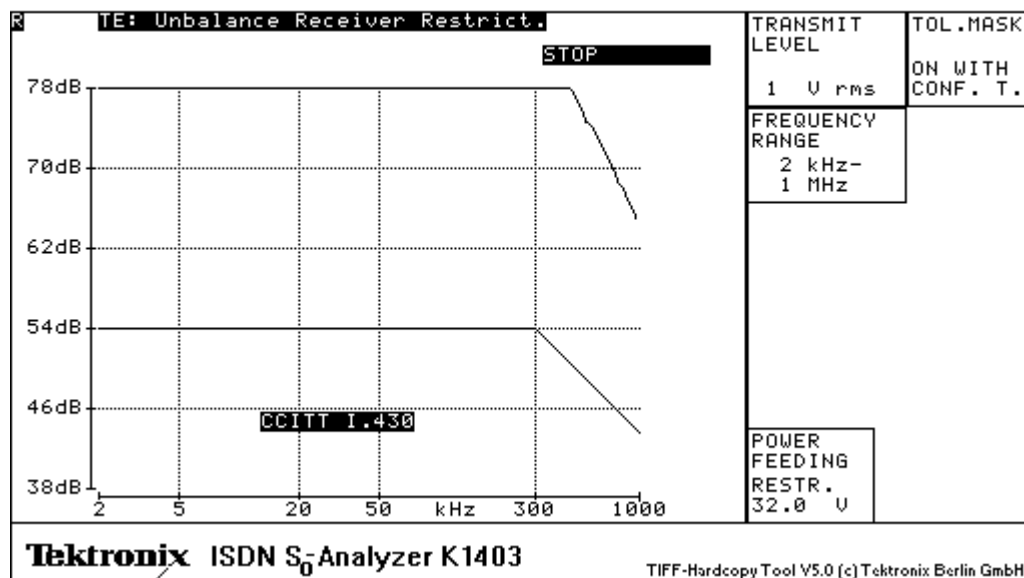
Receiver unbalance about earth (longitudinal conversion loss) in state F3, restricted power at 42V

Conformance PASSED

26. V30-18.4U

Receiver unbalance about earth (longitudinal conversion loss) in state F3, restricted power at 32V

Conformance PASSED



Input to output offset

config. I – binary ones, different jitter

1. V30-11.4a

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an in put sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.946393 %	tmax = 3.253628 %
tava = 2.853604 %	

2. V30-11.4b

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an in put sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.946393 %	tmax = 3.653653 %
tava = 0.853604 %	

3. V30-11.4c

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an in put sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.946393 %	tmax = 3.253628 %
tava = 2.453641 %	

4. V30-11.4d

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an in put sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -3.146359 %	tmax = 3.253628 %
tava = 0.853604 %	

config. I – octet 0AAH, different jitter

13. V30-11.24a

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D-and D-Echo channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.346371 %	tmax = 4.253629 %
tava = 2.653592 %	

14. V30-11.24b

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D-and D-Echo channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.146359 %	tmax = 4.453641 %
tava = 2.853604 %	

15. V30-11.24c

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D-and D-Echo channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.146359 %	tmax = 4.453641 %
tava = 2.053616 %	

16. V30-11.24d

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D-and D-Echo channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.946393 %	tmax = 4.453641 %
tava = 0.853604 %	

config. I – binary zeroes, different jitter

25. V30-11.44a

Input to output offset, point to point configuration (high cap. cable with 6 dB attenuation) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -0.946393 %	tmax = 4.253629 %
tava = 0.653592 %	

26. V30-11.44b

Input to output offset, point to point configuration (high cap. cable with 6 dB attenuation) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -1.546384 %	tmax = 4.453641 %
tava = 2.853604 %	

27. V30-11.44c

Input to output offset, point to point configuration (high cap. cable with 6 dB attenuation) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -0.746381 %	tmax = 4.453641 %
tava = 2.053616 %	

28. V30-11.44d

Input to output offset, point to point configuration (high cap. cable with 6 dB attenuation) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -2.346371 %	tmax = 4.853604 %
tava = 2.453641 %	

config. I – 2¹⁹-1 PRBS, different jitter

37. V30-11.64a

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -2.746381 %	tmax = 5.253629 %
tava = 0.053616 %	

38. V30-11.64b

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -2.146390 %	tmax = 5.053586 %
tava = 1.053586 %	

39. V30-11.64c

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -2.746381 %	tmax = 5.253629 %
tava = 0.453641 %	

40. V30-11.64d

Input to output offset, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. I
Conformance PASSED

tmin = -2.746381 %	tmax = 5.253629 %
tava = 0.853604 %	

config. II – binary ones, different jitter

5. V30-11.8a

Input to output offset, short passive bus configuration (high cap. cable with 2 μ s delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 5.588033 %	tmax = 9.188029 %	tava = 7.588025 %
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6. V30-11.8b

Input to output offset, short passive bus configuration (high cap. cable with 2 μ s delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 4.988011 %	tmax = 8.988027 %	tava = 7.788018 %
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7. V30-11.8c

Input to output offset, short passive bus configuration (high cap. cable with 2 μ s delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 5.188014 %	tmax = 9.188029 %	tava = 7.588025 %
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8. V30-11.8d

Input to output offset, short passive bus configuration (high cap. cable with 2 μ s delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 3.588035 %	tmax = 9.588023 %	tava = 7.588025 %
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config. II – octet 0AAH, different jitter

17. V30-11.28a

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 2.388026 %	tmax = 9.188029 %	tava = 7.188031 %
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18. V30-11.28b

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 5.188014 %	tmax = 9.188029 %	tava = 6.388017 %
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19. V30-11.28c

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 5.188014 %	tmax = 9.188029 %	tava = 7.188031 %
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20. V30-11.28d

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 3.188016 %	tmax = 9.188029 %	tava = 7.188031 %
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config. II – binary zeroes, different jitter

29. V30-11.48a

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.II
Conformance PASSED

tmin = 0.788022 %	tmax = 8.788033 %	tava = 7.988021 %
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30. V30-11.48b

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.II
Conformance PASSED

tmin = -0.211970 %	tmax = 8.588031 %
tava = 6.588033 %	

31. V30-11.48c

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.II
Conformance PASSED

tmin = 5.188014 %	tmax = 8.788033 %	tava = 5.588027 %
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32. V30-11.48d

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.II
Conformance PASSED

tmin = 2.188024 %	tmax = 8.588031 %	tava = 6.588033 %
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config. II – 2¹⁹-1 PRBS, different jitter

41. V30-11.68a

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 2.388026 %	tmax = 9.188029 %	tava = 8.788033 %
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42. V30-11.68b

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 2.388026 %	tmax = 9.188029 %	tava = 6.788035 %
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43. V30-11.68c

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = 5.188014 %	tmax = 9.188029 %	tava = 7.188031 %
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44. V30-11.68d

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. II
Conformance PASSED

tmin = -0.411986 %	tmax = 9.188029 %
tava = 6.788035 %	

config. IV – binary ones, different jitter

9. V30-11.20a

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.238419 %	tava = 8.238420 %
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10. V30-11.20b

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 3.438416 %	tmax = 10.638413 %	tava = 9.038434 %
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11. V30-11.20c

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 8.638415 %
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12. V30-11.20d

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 7.838426 %
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config. IV – octet 0AAH, different jitter

21. V30-11.40a

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 5.838432 %	tmax = 10.638413 %	tava = 9.038434 %
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22. V30-11.40b

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 8.638415 %
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23. V30-11.40c

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 3.038421 %	tmax = 10.238419 %	tava = 8.638415 %
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24. V30-11.40d

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 5.438414 %	tmax = 10.638413 %	tava = 8.238420 %
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config. IV – binary zeroes, different jitter

33. V30-11.60a

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 3.038421 %	tmax = 10.238419 %	tava = 8.638415 %
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34. V30-11.60b

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 8.638415 %
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35. V30-11.60c

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 5.838432 %	tmax = 10.238419 %	tava = 7.838426 %
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36. V30-11.60d

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 7.438431 %
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config. IV – 2¹⁹-1 PRBS, different jitter

45. V30-11.80a

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config.IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 8.638415 %
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46. V30-11.80b

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config.IV
Conformance PASSED

tmin = 3.438416 %	tmax = 10.238419 %	tava = 8.238420 %
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47. V30-11.80c

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config.IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.238419 %	tava = 9.438429 %
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48. V30-11.80d

Input to output offset, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config.IV
Conformance PASSED

tmin = 4.238429 %	tmax = 10.638413 %	tava = 7.038436 %
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config. IIIa – binary ones, different jitter

1. V30-11.12a

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.838430 %	tmax = 11.038432 %	tava = 9.038434 %
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2. V30-11.12b

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.838430 %	tmax = 11.038432 %	tava = 9.038434 %
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3. V30-11.12c

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 11.038432 %	tava = 9.438429 %
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4. V30-11.12d

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 5.838432 %	tmax = 11.038432 %	tava = 8.638415 %
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config. IIIa – octet 0AAH, different jitter

9. V30-11.32a

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 11.038432 %	tava = 10.638413 %
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10. V30-11.32b

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 13.038430 %	tava = 8.638415 %
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11. V30-11.32c

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.838430 %	tmax = 11.038432 %	tava = 9.838424 %
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12. V30-11.32d

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 3.838434 %	tmax = 11.038432 %	tava = 8.238420 %
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config. IIIa – binary zeroes, different jitter

17. V30-11.52a

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 10.638413 %	tava = 9.438429 %
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18. V30-11.52b

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.IIIa
Conformance PASSED

tmin = 7.838430 %	tmax = 11.038432 %	tava = 8.638415 %
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19. V30-11.52c

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 11.038432 %	tava = 9.838424 %
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20. V30-11.52d

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config.IIIa
Conformance PASSED

tmin = 2.238431 %	tmax = 11.038432 %	tava = 9.038434 %
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config. IIIa – 2¹⁹-1 PRBS, different jitter

25. V30-11.72a

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 11.438427 %	tava = 10.238419 %
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26. V30-11.72b

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 11.038432 %	tava = 9.838424 %
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27. V30-11.72c

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 7.438436 %	tmax = 11.038432 %	tava = 8.638415 %
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28. V30-11.72d

Input to output offset, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIa
Conformance PASSED

tmin = 5.438414 %	tmax = 11.038432 %	tava = 8.638415 %
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config. IIIb – binary ones, different jitter

5. V30-11.16a

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 10.238419 %
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6. V30-11.16b

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.638413 %	tava = 9.838424 %
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7. V30-11.16c

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.638413 %	tava = 7.438431 %
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8. V30-11.16d

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 2.238431 %	tmax = 10.638413 %	tava = 7.438431 %
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config. IIIb – octet 0AAH, different jitter

13. V30-11.36a

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.638413 %	tava = 8.638415 %
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14. V30-11.36b

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 9.438429 %
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15. V30-11.36c

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.438436 %	tmax = 10.638413 %	tava = 9.038434 %
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16. V30-11.36d

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with the octet 0AAH in both B-channels and binary ones in the D- and D-Echo channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 5.438414 %	tmax = 10.638413 %	tava = 9.838424 %
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config. IIIb – binary zeroes, different jitter

21. V30-11.56a

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 8.238420 %
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22. V30-11.56b

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 9.838424 %
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23. V30-11.56c

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 8.238420 %
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24. V30-11.56d

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 3.838434 %	tmax = 10.638413 %	tava = 8.638415 %
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config. IIIb – 2¹⁹-1 PRBS, different jitter

29. V30-11.76a

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 7.838426 %
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30. V30-11.76b

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.638413 %	tava = 8.638415 %
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31. V30-11.76c

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 7.038417 %	tmax = 10.238419 %	tava = 8.638415 %
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32. V30-11.76d

Input to output offset, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIb
Conformance PASSED

tmin = 3.838434 %	tmax = 10.638413 %	tava = 9.438429 %
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Receiver sensitivity

config. IIIa – different jitter, 1.5dB attenuated

1. V30-17.20a

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

2. V30-17.20b

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

3. V30-17.20c

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

4. V30-17.20d

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. IIIa – different jitter, 1.5dB gain

5. V30-17.24a

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

6. V30-17.24b

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

7. V30-17.24c

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

8. V30-17.24d

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.IIIa
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. IIIb – different jitter, 1.5dB attenuated

9. V30-17.28a

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB attenuated signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

10. V30-17.28b

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB attenuated signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

11. V30-17.28c

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB attenuated signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

12. V30-17.28d

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB attenuated signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. IIIb – different jitter, 1.5dB gain

13. V30-17.32a

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB gain signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

14. V30-17.32b

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB gain signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

15. V30-17.32c

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB gain signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

16. V30-17.32d

Receiver sensitivity with jitter, short passive bus configuration (low cap. cable with 2µs delay) with 7TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with a 1.5dB gain signal source, restricted power at 32V

config. IIIb
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. I – different jitter, 1.5dB attenuated, 200kHz noise

1. V30-17.4a

Receiver sensitivity with 200kHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

2. V30-17.4b

Receiver sensitivity with 200kHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

3. V30-17.4c

Receiver sensitivity with 200kHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

4. V30-17.4d

Receiver sensitivity with 200kHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. I – different jitter, 1.5dB attenuated, 2MHz noise

5. V30-17.8a

Receiver sensitivity with 2MHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

6. V30-17.8b

Receiver sensitivity with 2MHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

7. V30-17.8c

Receiver sensitivity with 2MHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

8. V30-17.8d

Receiver sensitivity with 2MHz sine wave noise and jitter, short point to point configuration (high cap. cable with 6dB attenuation) with a with a 1.5dB attenuated signal source, restricted power at 32V

config. I

Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. II – different jitter, 1.5dB attenuated

9. V30-17.12a

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

10. V30-17.12b

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

11. V30-17.12c

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

12. V30-17.12d

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB attenuated signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. II – different jitter, 1.5dB gain

13. V30-17.16a

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

14. V30-17.16b

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

15. V30-17.16c

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

16. V30-17.16d

Receiver sensitivity with jitter, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with a 1.5dB gain signal source, restricted power at 32V

config.II
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

config. IV – different jitter, 1.5dB gain

17. V30-17.36a

Receiver sensitivity with jitter, ideal configuration (direct connection UUT-TE to NT) with a 1.5dB gain signal source, restricted power at 32V

config. IV
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

18. V30-17.36b

Receiver sensitivity with jitter, ideal configuration (direct connection UUT-TE to NT) with a 1.5dB gain signal source, restricted power at 32V

config. IV
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

19. V30-17.36c

Receiver sensitivity with jitter, ideal configuration (direct connection UUT-TE to NT) with a 1.5dB gain signal source, restricted power at 32V

config. IV
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

20. V30-17.36d

Receiver sensitivity with jitter, ideal configuration (direct connection UUT-TE to NT) with a 1.5dB gain signal source, restricted power at 32V

config. IV
Conformance PASSED

BitErrorCount = 0.000000
BitErrorRate = 0.000000
Intervals = 1
ErroredIntervals = 0
time = 60.000000 s

Jitter characteristics

config. I

1. V30-10.4

Jitter characteristics when transmitting INFO3, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. I

UI(pp) = 7.400000 %	UI(rms) = 1.200000 %
UI(pp-Hold) = 7.800000 %	

4. V30-10.24

Jitter characteristics when transmitting INFO3, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of 40 frames with continuous octets of 0AAH in both B-channels an continuous ones in the D- and D-Echo channels followed by 40 frames with continuous binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. I

UI(pp) = 6.400000 %	UI(rms) = 1.000000 %
UI(pp-Hold) = 7.100000 %	

7. V30-10.44

Jitter characteristics when transmitting INFO3, point to point configuration (high cap. cable with 6dB attenuation) with an input sequence of continuous frames with a $2^{19}-1$ PRBS in D, D-Echo and both B channels, restricted power at 32V

config. I

UI(pp) = 9.700000 %	UI(rms) = 1.500000 %
UI(pp-Hold) = 10.000000 %	

config. II

2. V30-10.8

Jitter characteristics when transmitting INFO3, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. II

UI(pp) = 5.500000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.600000 %	

5. V30-10.28

Jitter characteristics when transmitting INFO3, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus, with an input sequence of 40 frames with continuous octets of 0AAH in both B-channels and continuous ones in the D- and D-Echo channels followed by 40 frames with continuous binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. II

UI(pp) = 5.300000 %	UI(rms) = 0.900000 %
UI(pp-Hold) = 5.300000 %	

8. V30-10.48

Jitter characteristics when transmitting INFO3, short passive bus configuration (high cap. cable with 2µs delay) with 8 TEs (including the UUT-TE) clustered at the far end of the bus with an input sequence of continuous frames with a 2¹⁹-1PRBS in D, D-Echo and both B channels, restricted power at 32V

config. II

UI(pp) = 5.800000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 6.300000 %	

config. IV

3. V30-10.20

Jitter characteristics when transmitting INFO3, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IV

UI(pp) = 5.000000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.000000 %	

6. V30-10.40

Jitter characteristics when transmitting INFO3, ideal configuration (direct connection TE to NT) with an input sequence of 40 frames with continuous octets of 0AAH in both B-channels an continuous ones in the D- and D-Echo channels followed by 40 frames with continuous binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IV

UI(pp) = 4.800000 %	UI(rms) = 1.000000 %
UI(pp-Hold) = 4.800000 %	

9. V30-10.60

Jitter characteristics when transmitting INFO3, ideal configuration (direct connection TE to NT) with an input sequence of continuous frames with a $2^{19}-1$ PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IV

UI(pp) = 5.200000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.200000 %	

config. IIIa

1. V30-10.12

Jitter characteristics when transmitting INFO3, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIa

UI(pp) = 5.100000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.200000 %	

3. V30-10.32

Jitter characteristics when transmitting INFO3, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of 40 frames with continuous octets of 0AAH in both B-channels and continuous ones in the D- and D-Echo channels followed by 40 frames with continuous binary zeroes in D, D-Echo and both B channels, restricted power at 32V

config. IIIa

UI(pp) = 4.800000 %	UI(rms) = 0.900000 %
UI(pp-Hold) = 4.900000 %	

5. V30-10.52

Jitter characteristics when transmitting INFO3, short passive bus configuration (high cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIa

UI(pp) = 5.600000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.700000 %	

config. IIIb

2. V30-10.16

Jitter characteristics when transmitting INFO3, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of continuous frames with all binary ones in D, D-Echo and both B channels, restricted power at 32V

config. IIIb

UI(pp) = 5.100000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.100000 %	

4. V30-10.36

Jitter characteristics when transmitting INFO3, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, and the UUT-TE adjacent to the signal source with an input sequence of 40 frames with continuous octets of 0AAH in both B-channels an continuous ones in the D- and D-Echo channels followed by 40 frames with continuous binary zeroes in D, -Echo and both B channels, restricted power at 32V

config. IIIb

UI(pp) = 4.800000 %	UI(rms) = 0.900000 %
UI(pp-Hold) = 4.900000 %	

6. V30-10.56

Jitter characteristics when transmitting INFO3, short passive bus configuration (low cap. cable with 2µs delay) with 7 TEs clustered at the far end of the bus, with the UUT-TE adjacent to the signal source, with an input sequence of continuous frames with a 2¹⁹-1 PRBS in D, D-Echo and both B channels, restricted power at 32V

config. IIIb

UI(pp) = 5.700000 %	UI(rms) = 1.100000 %
UI(pp-Hold) = 5.700000 %	